



Daffodil International University
Department of Computer Science and Engineering

Faculty of Science & Information Technology

Midterm Exam Examination, Fall 2020 @ DIU Blended Learning Center
Course Code: CSE322 (Day), Course Title: Computer Architecture & Organization

Level: 3 Term: 2 Section: All

Instructor: ALL Modality: Open Book Exam

Date: Sunday 8 November, 2020 Time: 02:00pm-06:00pm

Four hours (4:00) to support online open/case study based assessment Marks: 25

Directions:

- **Students need to go through the CASE STUDY shown in this exam paper.**
- **Analyze and answer specific section based on your own thinking and work.**
- **Do not share as this will be treated as plagiarism by Blended Learning Center.**

Life will give you many options, but today you have to “Answer all the questions”

- Q.1**
- a) Suppose, an Instruction has been given to CPU to compute 3 steps addition of an equation. Write and explain the internal structure of CPU and Cache memory for calculating 3 consecutive additions and Show the ALU gate level design and register level design for this addition (Give a short description and draw the circuit) **5**
- b) What is the difference between Architecture and Organization? Cite some practical examples based on your daily life. **2**
- Q.2**
- a) You have read about the assembly language. There you found a term called “Assembler”. In your computer which component is similar to this ancient component “Assembler”? How the present component of your computer is playing the role of “Assembler”? How will you explain the similarities? Write your answer with proper example and definition. **4**
- b) Analyze the scenario where the Computer and Human Brain works together and tries to play the board game of monopoly. Specify with your view what would be the drawback and benefit. **2**
- Q.3**
- a) Do you know about any early computational mechanical devices which is now a days used as a toy for children? If yes, please mention the name of the device and describe about it. **2**
- b) There are 3 design levels and each design level there are many types of components. Can you classify the following components according to their design level? **2**

Projector, NAND gate, Adder, Pen drive, counter, Cache Memory

- c) Analyze the scenario: what happens if the Cache Memory is only connected to the Main Memory? **2**

- Q.4**
- a)** If the Binary value of E is 1000101 then find out both the decimal and binary value of 1st five letters of your name (consider your full name if your 1st name is less than 5 letter). **3**
 - b)** If you want to seek the attention of processor of what should you do and How? **1.5**
 - c)** What do you understand by “Synthesis” in CPU design? **1.5**